

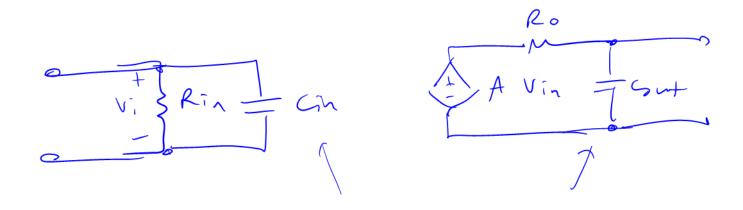
Lecture 2: Non-Ideal Amps and Op-Amps

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Practical Op-Amps

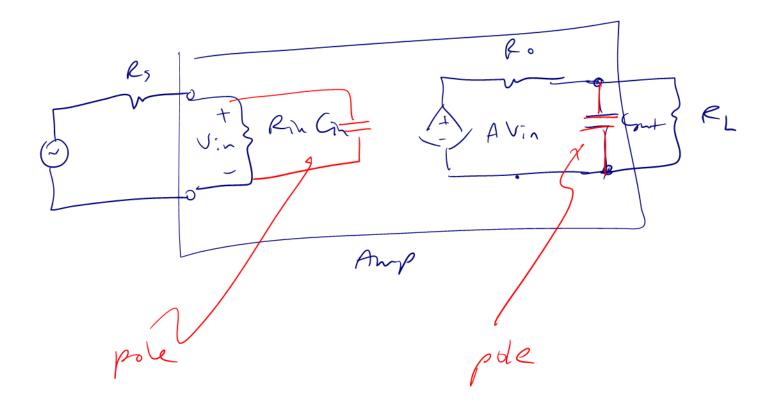
- Linear Imperfections:
 - Finite open-loop gain (A_0 < ∞)
 - Finite input resistance $(R_i < \infty)$
 - Non-zero output resistance $(R_o > 0)$
 - Finite bandwidth / Gain-BW Trade-Off
- Other (non-linear) imperfections:
 - Slew rate limitations
 - Finite swing
 - Offset voltage
 - Input bias and offset currents
 - Noise and distortion

Simple Model of Amplifier



- Input capacitance and output capacitance are added
- Any amplifier has input capacitance due to transistors and packaging / board parasitics
- Output capacitance is usually dominated by the load
 - Driving cables or a board trace
 - Intrinsic capacitance of actuator

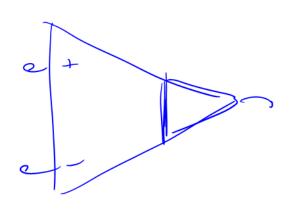
Transfer Function

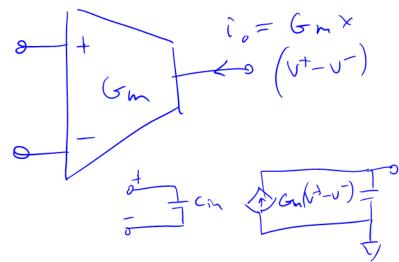


• Using the concept of impedance, it's easy to derive the transfer function

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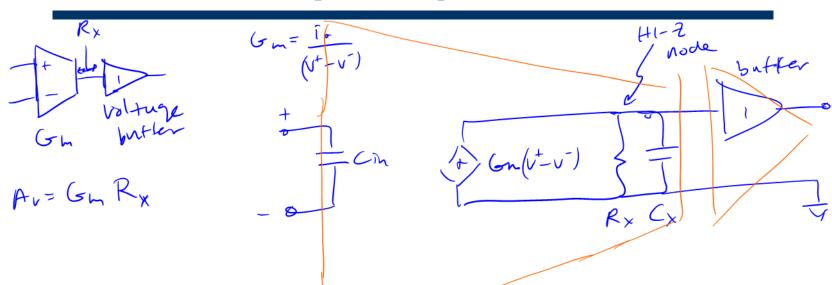
Operational Transconductance Amp





- Also known as an "OTA"
 - If we "chop off" the output stage of an op-amp, we get an OTA
- An OTA is essentially a $G_{\rm m}$ amplifier. It has a current output, so if we want to drive a load resistor, we need an output stage (buffer)
- Many op-amps are internally constructed from an OTA + buffer

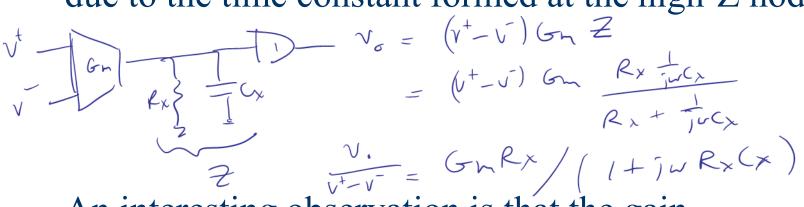
Op-Amp Model



- The following model closely resembles the insides of an op-amp.
- The input OTA stage drives a high Z node to generate a very large voltage gain.
- The output buffer then can drive a low impedance load and preserve the high voltage gain

Op-Amp Gain / Bandwidth

• The dominant frequency response of the op-amp is due to the time constant formed at the high-Z node



• An interesting observation is that the gainbandwidth product depends on G_m and C_x only

$$BW = \frac{1}{R \times C \times}$$

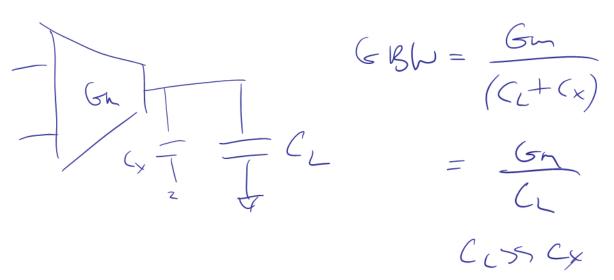
$$G_0 \times BU = \frac{1}{C \times}$$

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Preview: Driving Capacitive Loads

- In many situations, the load is capacitor rather than a resistor
- For such cases, we can directly use an OTA (rather than a full op-amp) and the gain / bandwidth product are now determined by the load capacitance

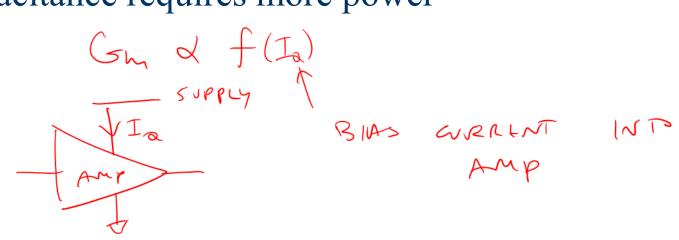


OTA Power Consumption

• For a fixed load, the current consumption of the OTA is fixed by the gain/bandwidth requirement, assuming load dominates

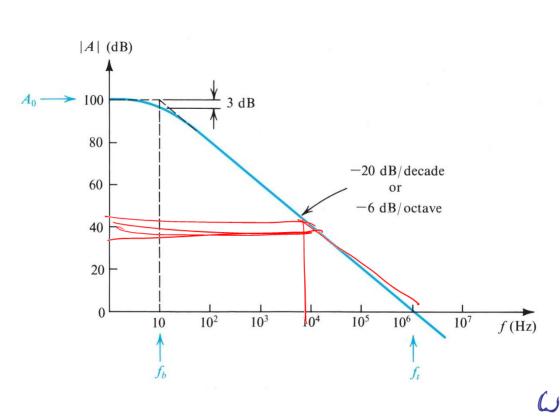
$$C_L$$
>> C_x

• $G_{\rm m}$ scales with current, so driving a larger capacitance requires more power



Gain/Bandwidth Trade-off

Open-Loop Frequency Response



$$A(j\mathbf{W}) = \frac{A_0}{1 + j\mathbf{W}/\mathbf{W}_b}$$

 A_0 : dc gain

 W_b : 3dB frequency

 $W_t = A_0 W_b$: unity-gain bandwidth (or "gain-bandwidth product")

For high frequency, $W >> W_h$

$$A(jw) = \frac{w_t}{jw}$$

$$\omega >> \omega_b \qquad \left(\frac{\partial \omega}{\partial b} \right) >> 0$$

$$\omega_b = \frac{\partial \omega}{\partial b}$$

Bandwidth Extension

• Suppose the core amplifier is single pole with bandwidth:

h:
$$G(\omega) = \frac{G(\omega)}{1 + j \frac{\omega}{\omega_b}}$$
BW

• When used feedback, the overall transfer function

is given by
$$G_{CL} = \frac{G}{1+G+} = \frac{G_{o}}{1+j\omega/\omega_{o}}$$

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$$G_{$$

Gain / Bandwidth Product in Feedback

- Even though the bandwidth expanded by (1+T), the gain drops by the same factor. So overall the gainbandwidth (GBW) product is constant
- The GBW product depends only the the G_m of the op-amp and the C_x internal capacitance (or load in the case of an OTA)

$$\omega_{b} = (1+T) \omega_{b}$$

$$G_{CL} = \frac{G}{1+T}$$

$$\omega_{b} G_{CL} = (1+T) \omega_{b}$$

$$\frac{G}{1+T} = GBW$$

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Unity Gain Frequency

- To see this, consider the frequency at which the gain drops to unity

$$|G(u_{1})| = \frac{|G_{0}|}{|G_{0}|} = \frac{|G_{0$$

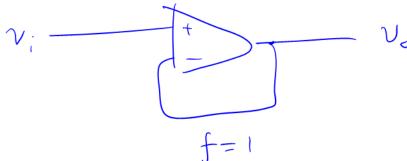
Unity Gain Feedback Amplifier

• An amplifier that has a feedback factor *f*=1, such as a unity gain buffer, has the full GBW product frequency range

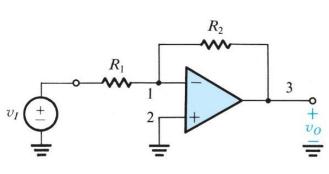
$$\omega_{b}' = \omega_{b} (1+T)$$

$$T = Gf = G$$

$$\omega_{b}' = \omega_{b} \cdot (1+G) = GBW! Full BANDWINTER$$



Closed-Loop Op Amp



$$\frac{v_1 - v_2}{R_1} = \frac{v_2 - v_3}{R_2}$$

$$V_{1} = \sqrt{\frac{1 + \frac{R_{1}}{R_{2}}}{R_{2}}} - \frac{R_{1}}{R_{2}} v_{0}$$

$$= -\frac{v_{0}}{G} \left(1 + \frac{R_{1}}{R_{2}}\right) - \frac{R_{1}}{R_{2}} v_{0} = -\frac{v_{0}}{G} \left(\frac{R_{1}}{R_{2}} + \frac{1}{G} + \frac{R_{1}}{R_{2}} \frac{1}{G}\right)$$

$$\frac{v_{1}}{V_{1}} = \frac{-\frac{R_{2}}{R_{1}}}{\frac{1}{G}} + \frac{1}{G} + \frac{1}{G}$$

$$\frac{V_{r}}{V_{i}} = \frac{-R_{2}/R_{1}}{\frac{1}{G}R_{1}^{2} + (\frac{1}{G}+1)} = \frac{-R_{2}(R_{1})}{\frac{(1+j)^{2}/G_{1}}{G_{1}} + (1) + \frac{R_{2}}{R_{1}}} \frac{(1+j)^{2}/G_{1}}{G_{0}}$$

$$= \frac{-R_{2}/R_{1} \cdot G_{0}}{1+j^{2}/G_{1}} = \frac{-\frac{R_{2}}{R_{1}}}{\frac{R_{1}}{G_{0}}} \frac{G_{0}/(1+G_{0}+\frac{R_{2}}{R_{1}})}{\frac{1+j^{2}/G_{0}}{G_{0}}}$$

$$= \frac{-R_{2}/R_{1} \cdot G_{0}}{1+j^{2}/G_{1}} = \frac{-\frac{R_{2}}{R_{1}}}{\frac{R_{1}}{G_{0}}} \frac{G_{0}/(1+G_{0}+\frac{R_{2}}{R_{1}})}{\frac{1+j^{2}/G_{0}+\frac{R_{2}}{R_{1}}}{\frac{R_{1}}{G_{0}}}$$

$$= \frac{-R_{2}/R_{1}}{R_{1}} \cdot G_{0}/(1+G_{0}+\frac{R_{2}}{R_{1}})$$

$$= \frac{-R_{2}/R_{1}}{R_{1}} \cdot G_{0}/(1+G_{0}+\frac{R_{2}}{R$$

$$DCGAIN: -\frac{RZ}{RI}\frac{G_0}{1+G_0+RZ} \simeq -\frac{RZ}{RI}$$

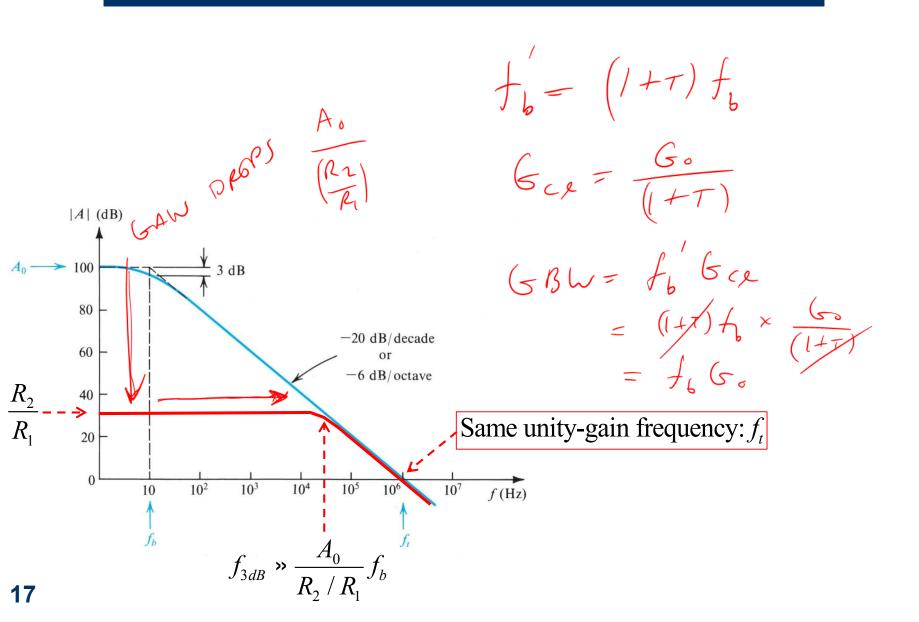
NEW BW:
$$\omega_b' = \omega_b \left(1 + G_0 + \frac{R_2}{R_1}\right)$$

$$\frac{1 + R_2}{R_1}$$

$$\frac{2}{1+\frac{R_2}{R_1}} = \frac{4 \times R_1}{1+\frac{R_2}{R_1}} = \frac{1}{1+\frac{R_2}{R_1}} = \frac{1}{1+\frac{R_2}{$$

EXPAYSION

Frequency Response of Closed-Loop Inverting Amplifier Example



Non-Dominant Poles

- As we have seen, poles in the system tend to make an amplifier less stable. A single pole cannot do harm since it has a maximum phase shift of 90°
- A second pole in the system is not affected by feedback (prove this) and it will add phase shift as the frequency approaches this second pole
- For this reason, non-dominant poles should be at a much higher frequency than the unity-gain frequency

Positive Feedback

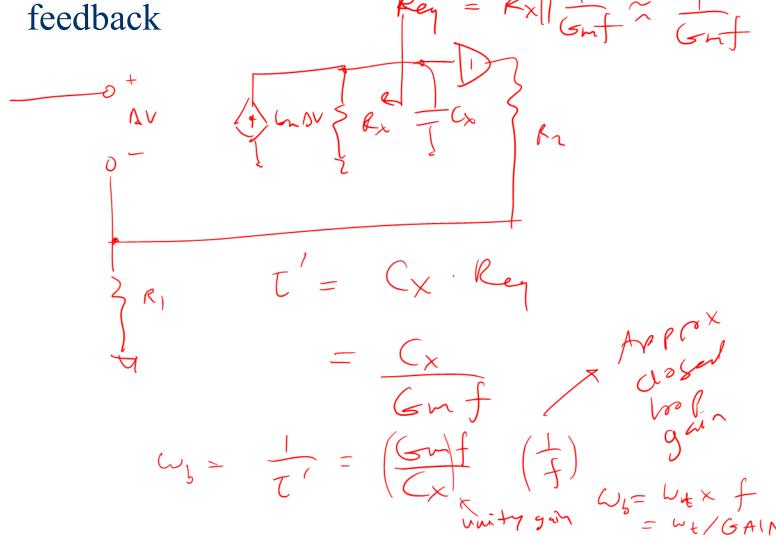
- Positive Feedback is also useful
- We can create a comparator circuit with *hysteresis*
- Also, as long as T < 1, we can get stable gain ... instead of reducing the gain (negative feedback), positive feedback enhances the gain.
- In theory we can boost the gain to any desired level simply by making T close to unity:

$$T = 1 - \epsilon$$

- ε is a very small number
 - In practice if the gain varies over process / temperature / voltage, then the circuit can go stable and oscillate
 - Positive feedback also has a narrow-banding effect

Back to Circuit Model

• Here's the equivalent circuit for an amplifier with



Circuit Interpretation

• Here we see the action of the feedback is to lower the impedance seen by the G_m by the loop gain, which expands the bandwidth by the same factor

Comment: This is advanced Stuff

So don't worry it the

Circuit interpretation is not

100%. Clear!